

SYSTEM 100

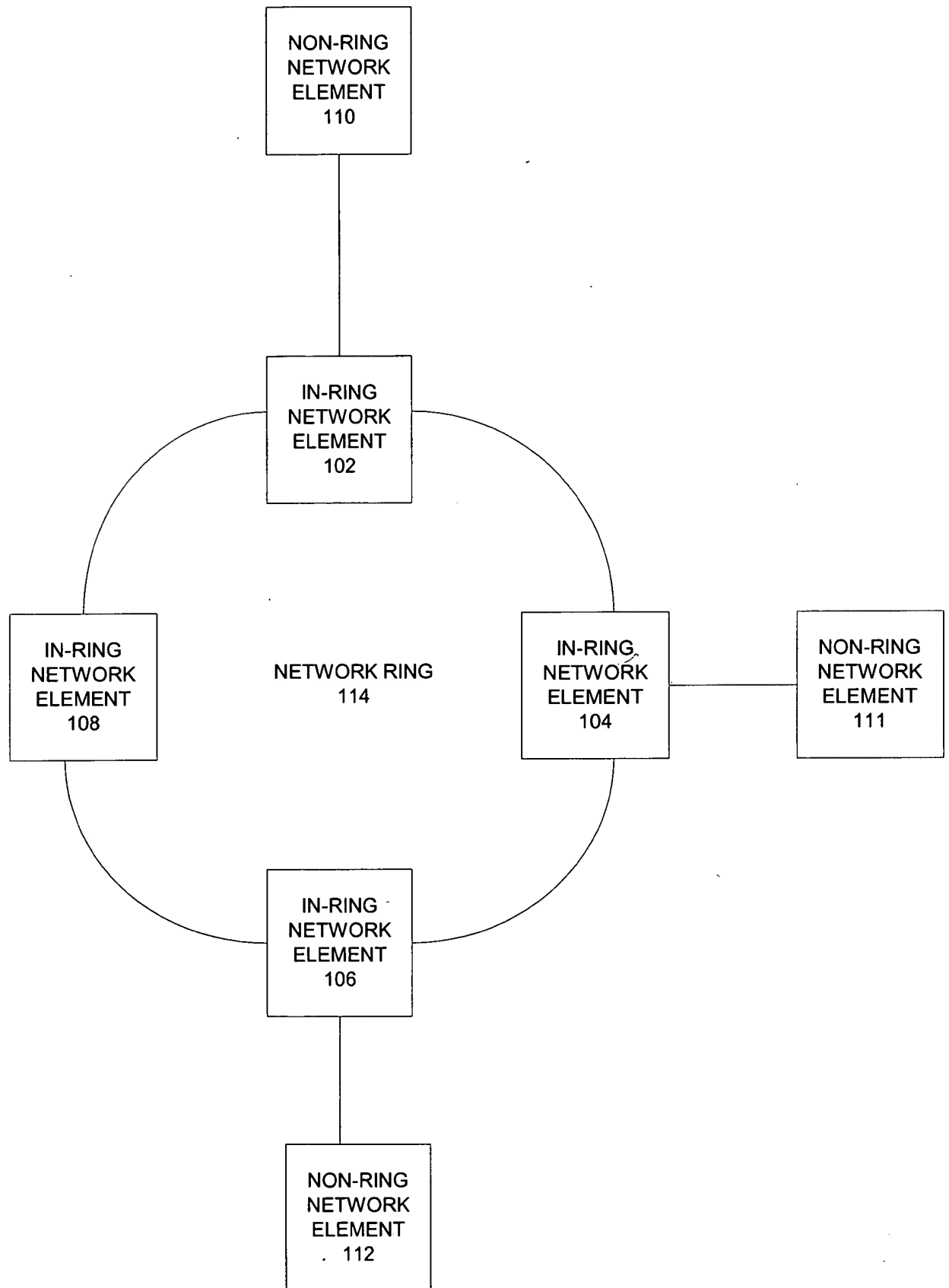


FIG. 1

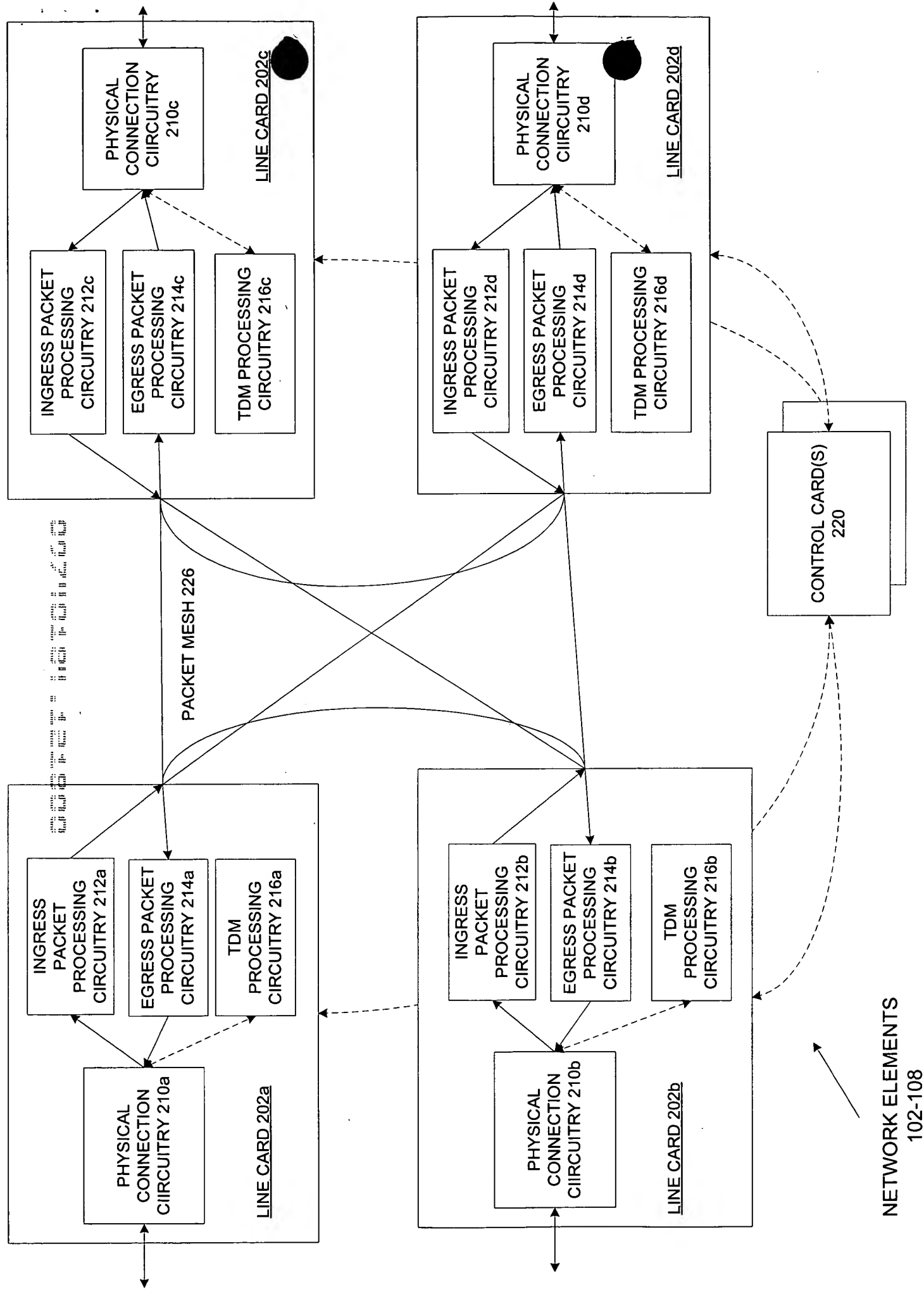


FIG. 2

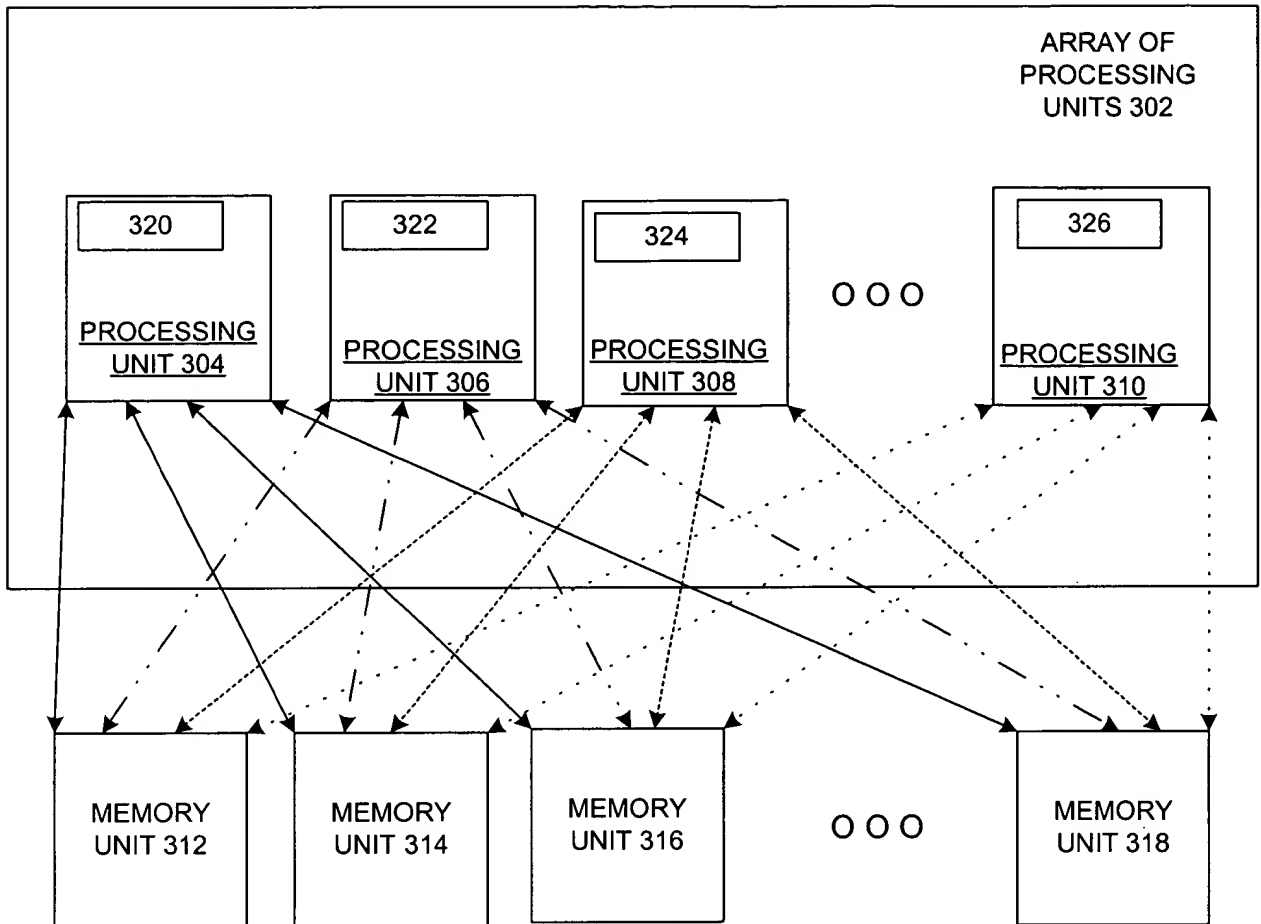


FIG. 3

METHOD 400

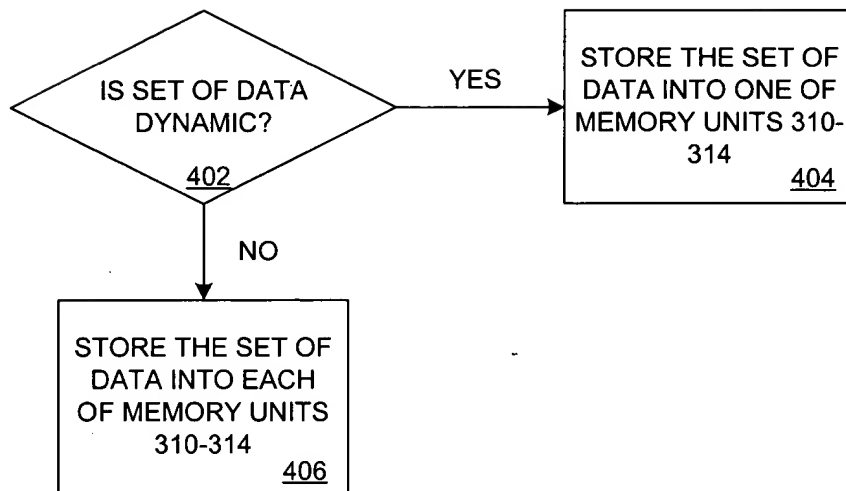
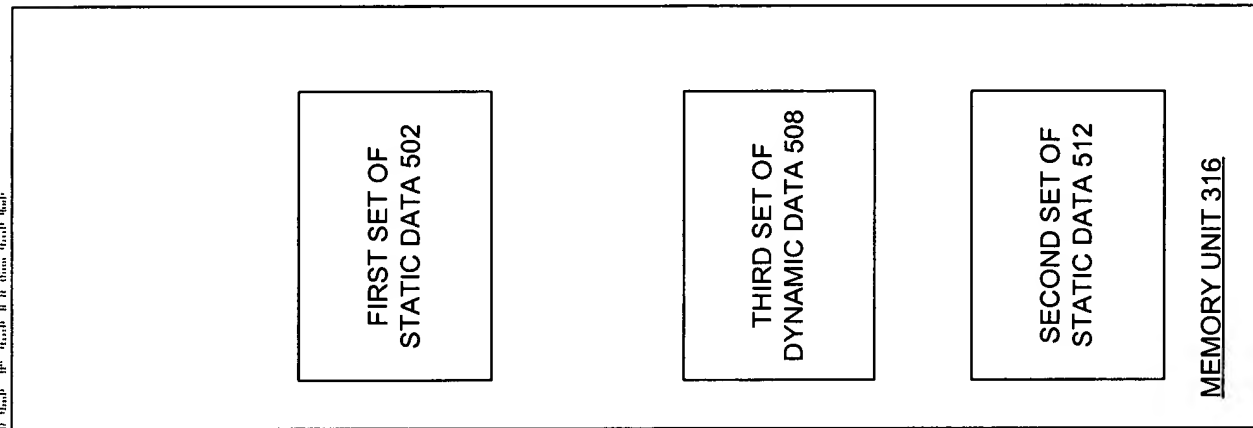
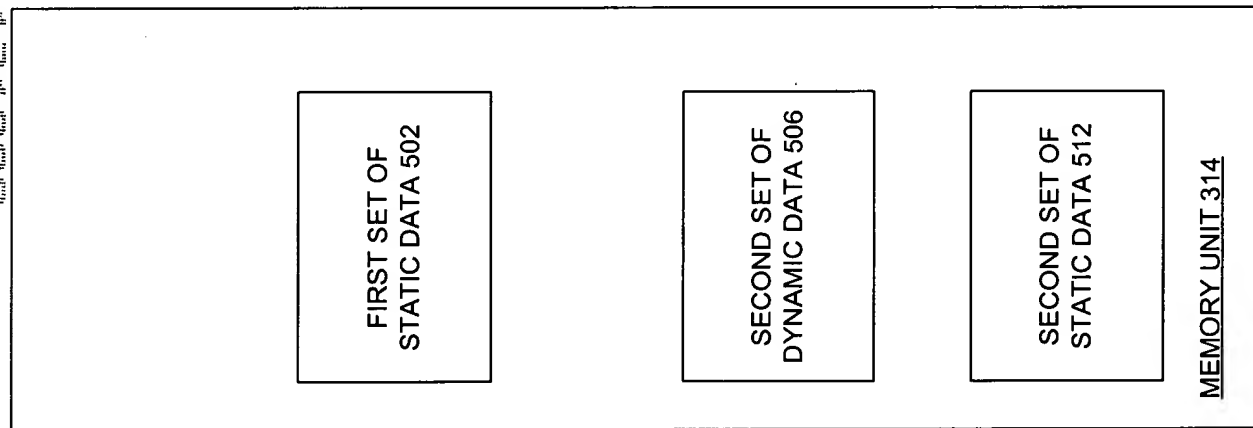
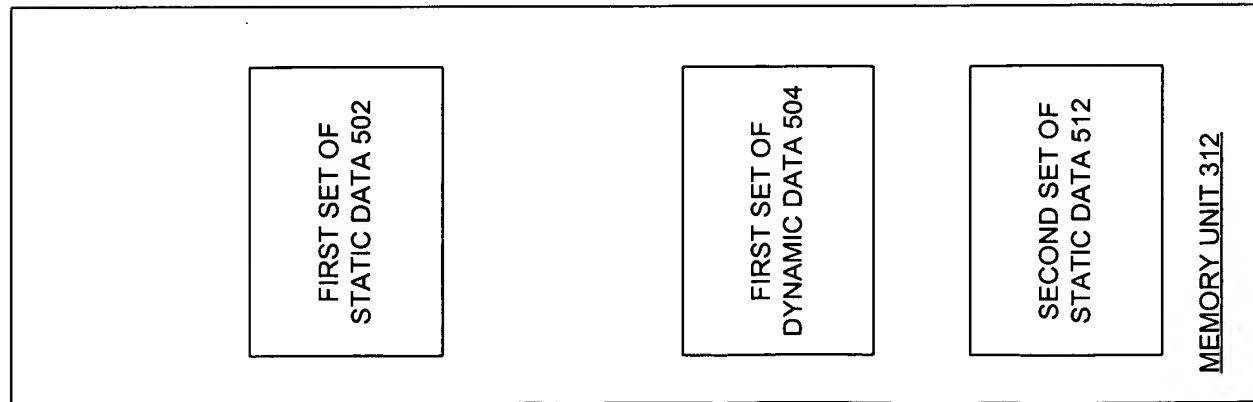


FIG. 4

FIG. 5 is a block diagram of a memory unit 310, which is a part of a system 100. The system 100 includes a processor 102, a memory unit 310, and a storage device 104. The memory unit 310 is connected to the processor 102 and the storage device 104. The memory unit 310 includes a first set of static data 502, a first set of dynamic data 504, and a second set of static data 512. The storage device 104 includes a first set of static data 502, a first set of dynamic data 504, and a second set of static data 512. The memory unit 310 is connected to the processor 102 and the storage device 104. The memory unit 310 includes a first set of static data 502, a first set of dynamic data 504, and a second set of static data 512. The storage device 104 includes a first set of static data 502, a first set of dynamic data 504, and a second set of static data 512.



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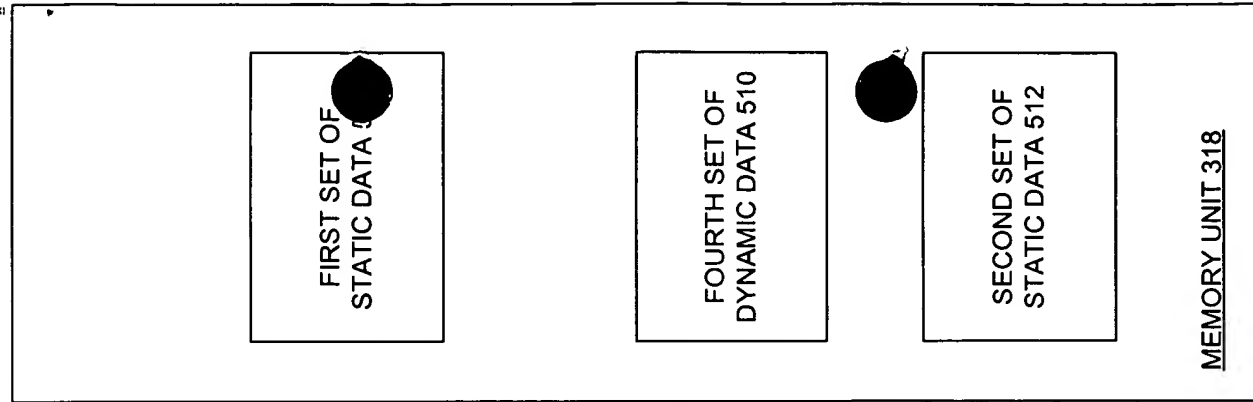
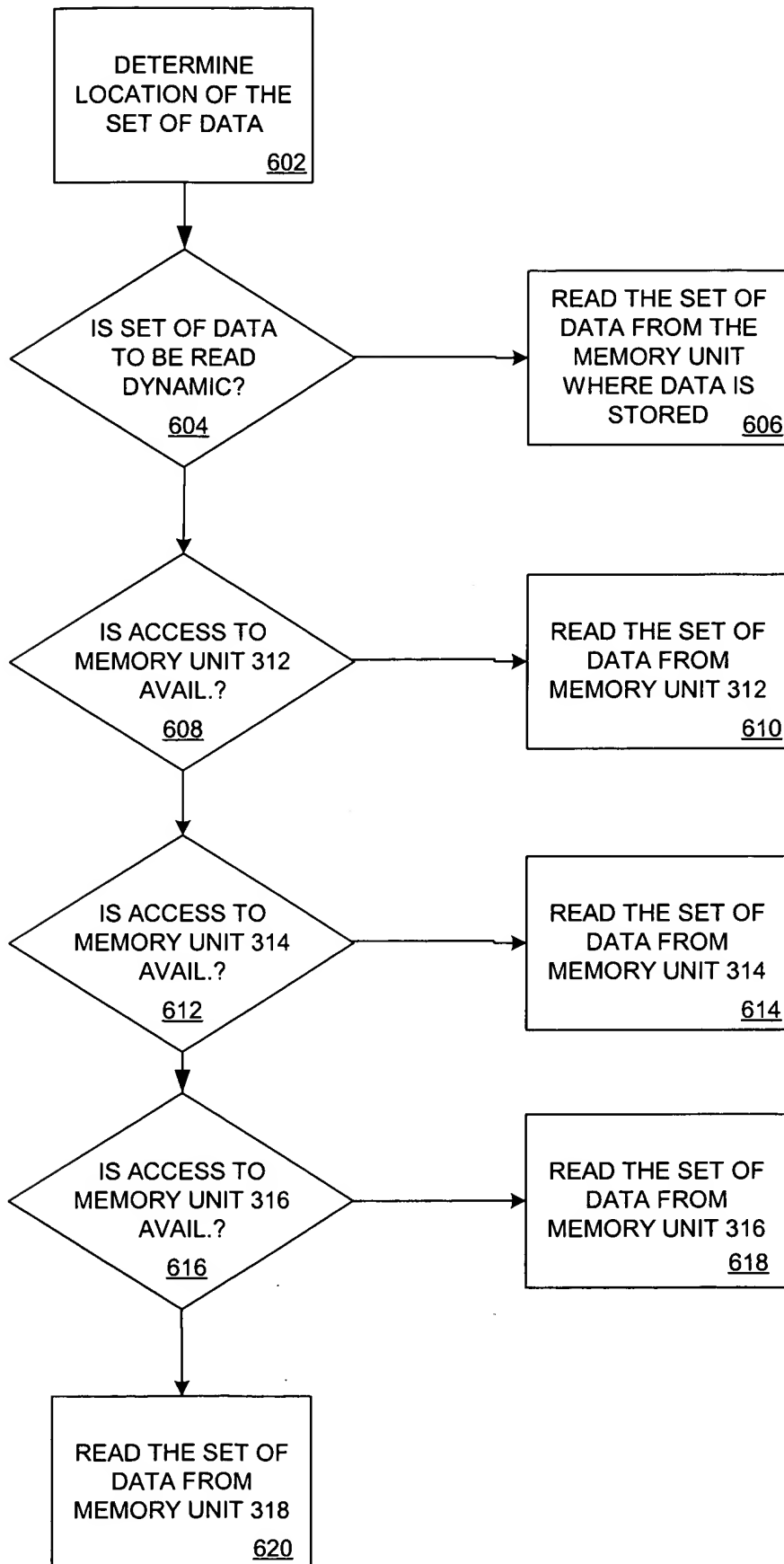


FIG. 5



METHOD 600

FIG. 6